

Awards

Name of Award	Organizers	Date of Award
Outstanding performance in 101 Academy Year College Student Participation in Research Projects	MCU Strive for Excellence Project Office	2012.09
99 Academy Year (Full-time Teacher) Excellence in Research Award	Strive for Excellence Project Office	2012.03
Private Education Association Model Teacher Award of R.O.C.	Private Education Association	2011.09

Research Achievements:Conference Paper

Article Title	Journal Name	Author	Category	Publication Date
Design of DC to 10-GHz broadband CMOS transmit/receive switch circuits	Microwave and Optical Technology Letters	Yih-Hsia Lin, Chun-Hsueh Chu, Jeng Gong ,	SCI EI	2010.02
A 900-MHz 30-dBm bulk CMOS Transmit/Receive switch using stacking architecture, high substrate isolation and RF floated body	Progress In Electromagnetics Research C	Yih-Hsia Lin, Chun-Hsueh Chu, Da-Chiang Chang, Jeng Gong, and Ying-Zong Juang,	SCI EI	2009.11
Design of CMOS T/R switch using high substrate isolation and RF floated body for 1.9-GHz applications	Microwave and Optical Technology Letters	Yih-Hsia Lin, Chun-Hsueh Chu, Da-Chiang CHang, Jeng Gong and Ying-Zong Juang	SCI EI	2009.09
A wideband 27-dBm CMOS T/R switch using stacking architecture, high substrate isolation, and RF floated body	International Journal of Electronics	Chun-Hsueh Chu, Yih-Hsia Lin, Da-Chiang Chang, Jeng Gong and Ying-Zong Juang	SCI EI	2009.09
A 5.7-GHz low noise amplifier with source-	Microwave and Optical	Chun-Hsueh Chu, I-Lun	SCI EI	2009.08

degenerated active inductor	Technology Letters	Huang, Yih-Hsia Lin and Jeng Gong		
The Cu contamination effect in oxynitride gate dielectrics	J. Electrochem. Soc.	Y. H. Lin, F. M. Pan, Y. C. Liao, Y. C. Chen, I. J. Hsieh, and A. Chin	SCI	2001.09
The strong degradation on 30 & Aring; oxide integrity contaminated by copper	J. Electrochem. Soc.	Y. H. Lin, Y. C. Chen, K. T. Chan, F. M. Pan, I. J. Hsieh, and A. Chin	SCI	2001.04
The effect of copper on gate oxide integrity	J. Electrochem. Soc.	Y. H. Lin, Y. H. Wu, A. Chin, and F. M. Pan	SCI	2000.11

Research Achievements:Journal Article

Article Title	Journal Name	Author	Category	Publication Date
Comparison between Routabilities of State-of-the-art Designs of Switch Blocks in FPGA	26th VLSI Design/CAD Symposium (VLSI 2015)	Mao-Hsu Yen, Huang-Kuang Yen, Yih-Hsia Lin, Pei-Ting Huang, and Pei-Jung Tsai		2015.08
FPGA 手勢辨識電子有聲書	2015 銘傳大學國際研討會	Wei-Zu, Chin 、Meng-Hong, Xie 、You-An, Jain 、Xiang-Zhi, Bai 、Yih-Hsia Lin		2015.03
VLSI Implementation of 8051 MCU with In-System Programming	Workshop on Computer Architecture, Embedded Systems, SoC, and VLSI/EDA, ICS 2014	Mao-Hsu Yen, Yeong-Chang Maa, Yih-Hsia Lin, Chih-Chen Lai and Shu-Ping Zheng		2014.08

Design A Touch Control Game Using Altera De2-70 Board	2014 銘傳大學國際研討會	Jia-Jyun, Hong、Yung-Jia, Dong、Wang-Lin, Yang, Yih-Hsia Lin		2014.03
Hyper-Universal Switch Network for FPIC Design	2013 Seventh International Conference on Complex, Intelligent, and Software Intensive Systems	Mao-Hsu Yen, Chu Yu, Yih-Hsia Lin, Chang-Hsien Chung		2013.04
An Architecture for Efficient One-Dimensional Multi-level Inverse Discrete Wavelet Transform	中華科技大學四十三週年校慶論文教學研討會	Chin-Fa Hsieh, Yih-Hsia Lin, Neng-Jye Hsu, Te-Ming Kung		2011.04
帶隙參考電路之實作	2011 銘傳大學國際學術研討會	林憶霞, 游劉佑, 葉俊宏, 陳鴻堯, 王于華, 吳柏毅, 林祐廷		2011.03
Bipolar Resistive Switching of Pt/MgO/Pt Non-volatile Memory Device	IEDMS	Jun-Jea Feng, Fu-Chien Chiu, Shu-Hao Chang, Lin, Yih-Hsia and Wen-Chieh Shih		2010.07
有效的一維二階離散小波轉換之 IC 設計	2008 第一屆機體光機電科技應用與發展學術研討會議程	許能傑 劉政忠、林憶霞 陳央霖 謝慶發		2008.12
以即時平台實現之二維一階離散小波轉換雛型設計	2008 第一屆機體光機電科技應用與發展學術研討會議程	許能傑、陳央霖、林憶霞、林清泉、劉政忠、謝慶發		2008.12
PSNR 運算處理器設計	中華技術學院四十週年校慶論文教學研討會	劉政忠, 陳央霖, 林憶霞, 許能傑, 謝慶發		2008.05

影像雜訊濾波器的 IC 設計	中華技術學院四十週年校慶論文教學研討會	謝慶發, 沈泰安, 林憶霞, 黃奉偉, 許能傑		2008.05
A DTW VLSI Chip for Computing the DSW Algorithm	2008 銘傳大學國際研討會	Mao-Hsu Yen, Yeong-Chang Maa, Yih-Hsia Lin, Shin-Yi Cheng, Chun-Lung Wu		2008.03
FPGA with rapid RTR for embedded systems	17th VLSI Design/CAD Symposium	Mao-Hsu Yen, Shin-Yi Cheng, Yih-Hsia Lin and Haw-Yun Shin		2006.08
Design of 100MHz Frequency Synthesizer	銘傳大學 2004 國際學術研討會	Y. H. Lin, F. H. Wang, L. M. Chu, and J. L. Chang		2004.03
VLSI implementation of polygonal FPGA design	14th VLSI Design/CAD Symposium	Mao-Hsu Yen, Shih-Chung Yi, Chin-Fa Hsieh and Yih-Hsia Lin		2003.08
The thickness dependent gate oxide integrity degradation by Cu contamination	43th Electronic Materials Conference (EMC)	Y. H. Lin, Y. C. Chen, F. M. Pan, I. J. Hsieh, and A. Chin		2000.08

Research Achievements:Patent

Patent Title	Country	Patent Type/No	Date Patent Received
互補式金氧半場效電晶體切換開關之電路結構及製程結構	中華民國		2009.08.